



	Subclass
	Class

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PATENT NUMBER

U.S. UTILITY Patent Application

1220 O.I.P.E. CTH PATENT DATE
IR LMI Q.A. ~~2023~~ ~~SH~~
SCANNED ~~2023~~ Q.A. ~~SH~~

APPLICATION NO. 09/753764	CONT/PRIOR	CLASS 712	SUBCLASS 228	ART UNIT 2183	EXAMINER Chen, Eddie Huisman
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SaiLesh Kottapalli

100

Method and apparatus for reducing components necessary for instruction pointer generation in a simultaneous multithreaded processor

PTO-2040
12/88

ISSUING CLASSIFICATION

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
	(Assistant Examiner) _____ (Date)				
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____				ISSUE FEE	
	(Primary Examiner) _____ (Date)			Amount Due	Date Paid
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER	
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